



## Verilog HDL digital system design and practice(Chinese Edition)

By LIU RUI QIANG. TONG ZHEN LI. YIN HONG JIAN.

paperback. Condition: New. Pages Number: 213 Publisher: Electronic Industry Pub. Date :2011-1-1. This book describes the hardware description language verilog hdl and circuit design method. a total of 11 chapters. the main contents include: verilog-level design. verilog basic syntax. verilog behavior description. modeling combinational logic. sequential logic modeling. simulation modeling for the class. all levels of verilog description form and circuit modeling. tasks and functions. preprocessor. verilog design and sy.



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