



Device-Level Modeling and Synthesis of High-Performance Pipeline ADCs

By Manuel Delgado-Restituto

Springer New York Nov 2014, 2014. Taschenbuch. Condition: Neu. Neuware - This book presents models and procedures to design pipeline analog-to-digital converters, compensating for device inaccuracies, so that high-performance specs can be met within short design cycles. These models are capable of capturing and predicting the behavior of pipeline data converters within less than half-a-bit deviation, versus transistor-level simulations. As a result, far fewer model iterations are required across the design cycle. Models described in this book accurately predict transient behaviors, which are key to the performance of discrete-time systems and hence to the performance of pipeline data converters. 224 pp. Englisch.



READ ONLINE
[8.33 MB]



Reviews

The publication is easy in read through safer to comprehend. It is actually loaded with wisdom and knowledge Its been printed in an extremely simple way and is particularly simply right after i finished reading through this pdf where actually modified me, affect the way i believe.

-- Ms. Clementina Cole V

This is the very best publication i have got read until now. It is definitely simplified but shocks within the fifty percent of the pdf. You may like how the article writer create this pdf.

-- Rosario Durgan