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## RADIATION STATUS OF SUB-65 NM ELECTRONICS (PAPERBACK)



Radiation Status of Sub-65 nm Electronics

NASA Technical Reports Server (NTRS), Jonathan A. Pellish

Bibliogov, United States, 2013. Paperback. Condition: New. Language: English . Brand New Book \*\*\*\*\* Print on Demand \*\*\*\*\*. Ultra-scaled complementary metal oxide semiconductor (CMOS) includes commercial foundry capabilities at and below the 65 nm technology node Radiation evaluations take place using standard products and test characterization vehicles (memories, logic/latch chains, etc.) NEPP focus is two-fold: (1) Conduct early radiation evaluations to ascertain viability for future NASA missions (i.e. leverage commercial technology development). (2) Uncover gaps in current testing methodologies and...

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- Authored by Jonathan a Pellish
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