



Power-Aware Computer Systems: Third International Workshop, PACS 2003, San Diego, CA, USA, December 1, 2003, Revised Papers

By -

Springer. Paperback. Book Condition: New. Paperback. 215 pages. Dimensions: 9.2in. x 6.1in. x 0.3in. Welcome to the proceedings of the 3rd Power-Aware Computer Systems (PACS 2003) Workshop held in conjunction with the 36th Annual International Symposium on Microarchitecture (MICRO-36). The increase in power and -ergy dissipation in computer systems has begun to limit performance and has also resulted in higher cost and lower reliability. The increase also implies -duced battery life in portable systems. Because of the magnitude of the problem, all levels of computer systems, including circuits, architectures, and software, are being employed to address power and energy issues. PACS 2003 was the third workshop in its series to explore power- and energy-awareness at all levels of computer systems and brought together experts from academia and industry. These proceedings include 14 research papers, selected from 43 submissions, spanning a wide spectrum of areas in power-aware systems. We have grouped the papers into the following categories: (1) compilers, (2) embedded systems, (3) microarchitectures, and (4) cache and memory systems. The first paper on compiler techniques proposes pointer reuse analysis that is biased by runtime information (i. e. , the targets of pointers are determined based on the likelihood of their occurrence at runtime) to map accesses to efficient memory access paths (e. g. , avoid tag match). Another paper proposes compiling multiple programs together so...



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