

Design of High-Speed Communication Circuits

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It is really an amazing pdf which i actually have possibly read. I really could comprehended almost everything using this published e pdf. Its been printed in an remarkably easy way and it is just soon after i finished reading through this book in which in fact changed me, modify the way in my opinion. (Jena Jacobi)

DESIGN OF HIGH-SPEED COMMUNICATION CIRCUITS



World Scientific Publishing Co Pte Ltd. Hardback. Condition: new. BRAND NEW, Design of High-Speed Communication Circuits, Ramesh Harjani, MOS technology has rapidly become the de facto standard for mixed-signal integrated circuit design due to the high levels of integration possible as device geometries shrink to nanometer scales. The reduction in feature size means that the number of transistor and clock speeds have increased significantly. In fact, current day microprocessors contain hundreds of millions of transistors operating at multiple gigahertz. Furthermore, this reduction in feature size also has a significant impact on mixed-signal circuits. Due to the higher levels of integration, the majority of ASICs possesses some analog components. It has now become nearly mandatory to integrate both analog and digital circuits on the same substrate due to cost and power constraints. This book presents some of the newer problems and opportunities offered by the small device geometries and the high levels of integration that is now possible. The aim of this book is to summarize some of the most critical aspects of highspeed analog/RF communications circuits. Attention is focused on the impact of scaling, substrate noise, data converters, RF and wireless communication circuits and wireline communication circuits, including high-speed I/O.

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