

High Performance Carry Select Adder Using Binary Excess Converter

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Reviews

The book is great and fantastic. It is writter in straightforward words and phrases rather than difficult to understand. You wont really feel monotony at at any time of your respective time (that's what catalogues are for regarding should you question me). (Payton Miller)

HIGH PERFORMANCE CARRY SELECT ADDER USING BINARY EXCESS CONVERTER



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GRIN Verlag Gmbh Feb 2015, 2015. Taschenbuch. Book Condition: Neu. 210x148x1 mm. This item is printed on demand - Print on Demand Neuware - Scientific Essay from the year 2012 in the subject Electrotechnology, Priyadarshini College of Engineering, Nagpur, language: English, abstract: Adders are one of the widely used digital components in digital integrated circuit design. Addition is the basic operation used in almost all computational systems. Therefore, the efficient implementation and design of arithmetic units requires the binary adder structures to be implemented in an equally efficient manner. A ripple carry adder has smaller area but less speed. A carry look-ahead adder is faster though its area requirements are high. Carry select adders (CSLA) lie in middle. In this work a novel carry select adder using Binary Excess Converter (BEC) is proposed. It provides good compromise between cost and performance thereby establishing a proper trade-off between time and area complexities. In this work Tanner EDA is used for the comparison of all adders Ripple carry adder, Bitwise carry select adder, Square root carry select adder, proposed carry select adder using BEC. 12 pp. Englisch.

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